

Total No. of Printed Pages: 2

SUBJECT CODE NO: BB-2346
FACULTY OF SCIENCE AND TECHNOLOGY
B.Sc. (CBCGS) (Pattern 2022) T.Y SEM V
Examination November / December 2025
Electronics-XVIII A Microcontroller-I

[Time: 1:30 Hours]**[Max. Marks: 40]**

Please check whether you have got the right question paper.

N. B

- 1) Attempt all questions.
- 2) All questions carry equal marks.
- 3) Use only blue or black pen.
- 4) Draw flow chart/ Diagram whenever necessary.

Q1 Attempt any one.**10**

- a) Draw the labelled block diagram of 8051 microcontrollers?
- b) Explain 8051 microcontroller Addressing Modes?

Q2 Attempt any one.**10**

- a) Explain interrupts in MCS 8051.
- b) Explain pin diagrams of 89C51.

Q3 Write short notes on any two.**10**

- a) CISC and RISC processor.
- b) Logical instruction
- c) pin description of 89C2051
- d) Plus width modulation (PWM)

Q4 Attempt the following 10 multiple choice questions.**10**

1. PSEN full form ____ ?
 a) Program storage enable b) Point storage enable
 c) Plaint storage enables d) None of the above
2. The vector address of timer zero interrupt is _____.
 a) 03 H b) 0 BH c) 13 H d) 1 BH
3. 8051 microcontroller has _____ on chip RAM.
 a) 64 byte b) 64 k byte c) 128 byte d) 128 k byte
4. How many timers exist in 8051 which is 16 bit?
 a) 0 b) 1 c) 2 d) 3

5. The default value of register SP is ____
a) 37 H b) 27 H c) 17 H d) 07 H
6. Timer modes are set using ____ register.
a) TCON b) TMOB c) THO d) TL1
7. RST pin of 8051 Microcontroller is made high for ____ machine cycles.
a) 1 b) 2 c) 3 d) 4
8. When Timers TO or TI operated in mode 0, the timer is ____ bit wide.
a) 10 bit b) 8 bit c) 16 bit d) 13 bit
9. MCS-51 has an ____ bit accumulator.
a) 16 b) 24 c) 08 d) 36
10. 89C51 ____ on chip flash program memory.
a) 1k b) 2k c) 3k d) 4k

