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FACULTY OF SCIENCE & TECHNOLOGY
B.Sc. S. Y. (Sem-III)
Examination November/December-2022
Electronics Paper-
8085 Microprocessor – I VIII (B)

[Time: 1:30 Hours]   **[Max. Marks: 50]**

"Please check whether you have got the right question paper."

N.B.   i.   Attempt all questions.
                        ii. Illustrate your answers with suitable labelled diagram.

Q. 1   Draw the block diagram of architecture of 8085 microprocessor. Explain   20
            i)   Address bus
            ii)   data bus
            iii)   General purpose registers.

OR

            What is addressing modes? Explain different types of addressing modes in 8085   20
            microprocessor with suitable example.

Q. 2   Explain the DE multiplexing of the address bus $AD_7 – AD_0$ using schematic diagram of   20
            latching low - order Address Bus.

OR

            Write Short notes on any four from the following :   20
            a.   i. LDAX rp
                 ii. ORA M
            b.   Conditional jump instruction
            c.   Classification of instruction set
            d.   Features of 8085 microprocessor
            e.   Interrupt signals
            f.   Data transfer group of instruction

Q. 3   Attempt the following multiple choice questions.   10

         1. To operate IC 8085 microprocessor, the power supply requires _____ volt.
             a. +5 V   c. +10V
             b. -5 V   d. -10V

         2. INTA Stands _____.
             a. Interrupt request
             b. Interrupt Acknowledge
             c. Interrupt trap
             d. All of these

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3. Stack pointer is _____ bit register.
   a. 8
   b. 16
   c. 32
   d. 64

4. In 8085 microprocessor, result of arithmetic operation is stored in _____.
   a. Accumulator
   b. Register B
   c. Register pair BC
   d. Register pair HL

5. MVI A, 82H is _____ byte instruction
   a. 1   c. 3
   b. 2   d. 4

6. In 8085 microprocessor, to copy the number from B to A is written as _____ instruction.
   a. MOV A, B
   b. MOV B, A
   c. MVI A
   d. MVI B

7. The machine control instruction in 8085 microprocessors are _____.
   a. HLT
   b. NOP
   c. IN
   d. Both a and b

8. The 8085 microprocessor has _____ instruction.
   a. 78   c. 74
   b. 80   d. 95

9. Starting address of Stack is at _____.
   a. Stack top
   b. Stack bottom
   c. Program Counter
   d. Stack middle.

10. The 8085 microprocessor operates at _____.
    a. 2 MHZ Clock frequency
    b. 3 MHZ Clock frequency
    c. 6 MHZ Clock frequency
    d. Any frequency.

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