

Total No. of Printed Pages:3

SUBJECT CODE NO:- B-2150
FACULTY OF SCIENCE & TECHNOLOGY
B.Sc. S. Y. (Sem-III)
Examination November/December-2022
Electronics Paper-
8086 Microprocessor -VIII (A)

[Time: 1:30 Hours]

[Max. Marks:50]

“Please check whether you have got the right question paper.”

- N.B
- i. Attempt all questions.
 - ii. Illustrate your answer with suitable labelled diagram.
- Q.1 Draw and discuss the internal block diagram of 8086. 20
- OR
- State and explain the different instruction format of 8086 microprocessor. Hence write an ALP to find addition of two 8 bit numbers. 20
- Q.2 Draw the pin diagram of 8086 microprocessor. Explain the function of following pins. 20
- i) INTR
 - ii) AD₁₅ – AD₀
 - iii) TEST
 - iv) NMI
 - v) BHE / S₇
- OR
- Write Short notes on any four of the following. 20
- a. Status queue register
 - b. Segment register
 - c. ALU of 8086
 - d. Read and write cycle timing diagram of 8086 in maximum mode
 - e. Flag manipulation instruction
 - f. Addressing modes of 8086
- Q.3 Attempt the following multiple choice questions. 10
- 1) The BIU contains FIFO register of size _____ bytes.
 - a. 8
 - b. 6
 - c. 4
 - d. 12

- 2) The register to register instruction format has a length of _____.
 - a. 2 bytes
 - b. 4 bytes
 - c. 5 bytes
 - d. 3 bytes

- 3) The work of EU is
 - a. Encoding
 - b. Decoding
 - c. Processing
 - d. Register

- 4) The Flag that acts as Borrow Flag in the instruction SBB is _____.
 - a. Direction Flag
 - b. Carry Flag
 - c. Parity Flag
 - d. Trap Flag

- 5) Which of the following is not a basic element with in microprocessor
 - a. ALU
 - b. Register array
 - c. Control unit
 - d. Micro controller

- 6) Which is not a part of execution unit in an 8086 microprocessor
 - a. General purpose register
 - b. Flags
 - c. Clock
 - d. ALU

- 7) Which BUS is bidirectional
 - a. Data BUS
 - b. Control BUS
 - c. Address BUS
 - d. None of given

- 8) The SS is called as
 - a. Single Stack
 - b. Stack Segment
 - c. Sequence Memory
 - d. Random Stack

- 9) The instruction Format- “register to register” has a length of _____ bytes.
 - a. 4
 - b. 3
 - c. 2
 - d. 1

- 10) The register AX is formed by grouping
 - a. AH & AL
 - b. BH & BL
 - c. CH & CL
 - d. DH & DL