Total No. of Printed Pages:2

SUBJECT CODE NO:- 2120 FACULTY OF SCIENCE AND TECHNOLOGY

B.Sc. F.Y Sem-II

Examination March/April-2022 (To be held in June/July-2022) Electronics Paper-IV Amplifiers

[Time:	1:53 Hours] [Max. M	arks:50
N.B	Please check whether you have got the right question paper. 1) Attempt all questions. 2) Illustrate your answer with suitable diagram	S. Kr
Q.1	With neat circuit diagram. Explain the working of base bias or self bias circuit and hence derive the expressions for I_{CQ} & V_{CCQ} draw D.C load line	20
	OR	
	Draw a neat circuit diagram of direct coupled amplifier & explain its working	20
Q.2	Explain how feedback affects the output impedance when	20
	1) Feedback work in shunt with output	
	2) Feedback works in series with output	
	Write short notes on the following any four	20
	a) Load line of transistor	
	b) Two port network	
	c) Feedback principle	
90 X	d) Band width	
	e) Frequency response	
	f) Decibel gain	
Q.3	Attempt following MCQ with correct answer	10
	1. The ∝ for transistor is	
3 6 6 K	a) $\frac{\overline{l_B}}{\overline{l_E}}$	

	c)	$\frac{I_C}{I_E}$		d)	$\frac{I_E}{I_C}$			
2.	Voltage	e gain A θ is the ratio of			(3) (4) (8) (4) (4) (4) (4) (4) (4) (4) (4) (4) (4			
	_	$\vartheta i/\vartheta_o$		h)	$\frac{\vartheta_i}{\vartheta_i}$			
		190		.07	$\frac{\vartheta_i}{I_C}$ $\frac{\vartheta_o}{I_o}$			
	c)	$rac{artheta_o}{artheta_i}$	E S	d)	I_{o}			
3.	3. If the absolute power gain of an amp. Is 100 then its decibel gain							
	,	40∂B) 0) 100∂ <i>B</i>			
	c)	60∂B		d) $20\partial B$			
4.	The β f	or the transistor is						
			2) 8 / K	b)				
		$\frac{I_E}{I_C}$ $\frac{I_B}{I_C}$		3/1/2	$\frac{\underline{I_C}}{\overline{I_E}}$ $\frac{\underline{I_C}}{\overline{I_D}}$			
	c)	I _C		d)	$\overline{I_{b}}$			
5.	The gai	n of the amplifier after feedback						
		Increases	283	b)	Decreases			
	c)	Remains constant		d)	None Above			
6.	Hybrid	parameter means	12 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	37				
	a)	Resistance parameter	b)	Ad	mittance parameter			
	c)	Mixed parameters	d)	Vo	ltage parameters			
7.	7. Single battery operation are possible inamplifier.							
		C.B.	b) C	Œ.				
	c)	C.C.	d) C	26				
_								
8.	7/X, YY 7 7, U. Y	perating point changes shape of the outpu	.05-					
15°	. 6 0 0 0	Faithfull The arrest and account		-	Un-faithful			
		Thermal runaway	'	u)	Stabilization			
9.	In any mode the working of transistor is –in –bias							
	LY 6 1 1 1 1 1 -	Forward-reverse		-	Forward- forward			
2/2/2	(3) (C)	Releasers – forward		d)	Reverse-reverse			
10. In common collector amplifier the voltage gains								
4 V	a)		b) {	gm.RL			
	(c)	–gm.RL	d	l)]	None			