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**SUBJECT CODE NO:- 2151**  
**FACULTY OF SCIENCE AND TECHNOLOGY**  
**B.Sc. S.Y Sem. III**  
**Examination March/April-2022 (To Be Held In June/July-2022)**  
**(B) 8085 Microprocessor – I VIII (B)**

[Time: 1:53 Hours]

[Max.Marks:50]

Please check whether you have got the right question paper.

- N.B. 1. Attempt all Questions.
- Q.1 With the help of a neat block diagram explain architecture of 8085 microprocessor. 20
- OR
- Explain instruction fetching and execution operation in 8085 microprocessor with an example. 20
- Q.2 Explain the logical group of instruction with example in 8085 microprocessor. 20
- OR
- Write Short notes on any four from the following. 20
- Concept of looping in assembly language programming
  - Flag register
  - Features of Intel 8085
  - Classification for 8085 microprocessor instruction set
  - Programming techniques in 8085 microprocessor
  - Conditional jump instructions in 8085 microprocessor.
- Q.3 Attempt the following multiple choice questions by selecting correct option. 10
- 8085 microprocessor has \_\_\_\_\_ pins.
    - 40
    - 30
    - 20
    - 10.
  - In 8085 microprocessor data bus is \_\_\_\_\_.
    - 16-bit
    - 8-bit
    - 4-bit
    - 64-bit
  - DMA stands for \_\_\_\_\_.
    - Direct memory access
    - Direct mode accumulator
    - Direct mode access
    - None of the above.

4. ALE stands for \_\_\_\_\_.
  - a) Arithmetic logic enable
  - b) Arithmetic latch enable
  - c) Address latch enable
  - d) none of the above.
  
5.  $\overline{MEMW}$  Stands for \_\_\_\_\_.
  - a) memory read
  - b) memory write
  - c) memory read and write
  - d) None of the above.
  
6. The accumulator is an 8-bit general purpose register connected through \_\_\_\_\_ and to ALU.
  - a) external data bus
  - b) internal data bus
  - c) temporary register
  - d) None of these.
  
7. Each instruction has a unique
  - a) Operand
  - b) Opcode
  - c) addressing mode
  - d) none of the above
  
8. In \_\_\_\_\_ addressing mode the address of the operand is given in the instruction itself.
  - a) Direct
  - b) Register
  - c) Implicit
  - d) None of the above.
  
9. Most of the instructions in logical groups affects.
  - a) carry flag
  - b) parity flag
  - c) Auxiliary flag
  - d) All of the above.
  
10. The \_\_\_\_\_ logical group perform addition operation.
  - a) AND
  - b) OR
  - c) EX-OR
  - d) all of the above