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SUBJECT CODE NO:- 2150
FACULTY OF SCIENCE AND TECHNOLOGY
B.Sc. S.Y Sem. III
Examination March/April-2022 (To Be Held In June/July-2022)
Electronics Paper-
(A) 8086 Microprocessor -VIII (A) OR

[Time:1:53
Hours]

[Max.Marks:50]

Please check whether you have got the right question paper.

- N.B
- i) Attempt all questions
 - ii) Illustrate your answer with suitable labelled diagram.
- Q.1 Draw and discuss the internal block diagram of 8086 microprocessor and explain the bus interface unit inil. 20
- OR
- What is an instruction? Explain any three instruction of data transfer and arithmetic operation. 20
- Q.2 What are different addressing modes in 8086 microprocessor. Explain them with suitable examples. 20
- OR
- Write short notes on any four of the following. 20
- a. Read and write cycle timing diagram of 8086 in minimum mode.
 - b. ROR and RLC instruction
 - c. General purpose registers.
 - d. Arithmetic instructions
 - e. Branch instructions
 - f. String instructions
- Q.3 Attempt all of the following multiple choice question 10
- 1) The mnemonic that is placed before the arithmetic operation performed is _____
 - a) AAA
 - b) AAS
 - c) AAM
 - d) AAD
 - 2) The stack pointer is decremented by _____ after each execution of PUSH instruction
 - a) One
 - b) Two
 - c) Four
 - d) Six
 - 3) Which of following is not a data copy/transfer instruction.
 - a) MOV
 - b) PUSH
 - c) DAS
 - d) POP
 - 4) The expansion of DAA is
 - a) Decimal adjust after addition
 - b) Decimal adjust before addition

- c) Decimal adjust accumulator
 - d) Decimal adjust auxiliary
- 5) Which of the following is not a machine control instruction
- a) Lock
 - b) ESC
 - c) HLT
 - d) CLC
- 6) The Intel 8086 microprocessor is a _____ bit processor.
- a) 32
 - b) 16
 - c) 8
 - d) 4
- 7) The flag that acts as borrow flag in the instruction SBB is _____ flag.
- a) Direction
 - b) Carry
 - c) Parity
 - d) Trap
- 8) The BIU contains FIFO register of size _____ bytes.
- a) 8
 - b) 6
 - c) 4
 - d) 12
- 9) The If is called as
- a) Initial flag
 - b) Indicate flag
 - c) Interrupt flag
 - d) Inter flag
- 10) The BIU pre fetches the instructions from memory or stores them in _____
- a) Queue
 - b) Register
 - c) Memory
 - d) Stack