Total No. of Printed Pages:2

SUBJECT CODE NO:- 2178 FACULTY OF SICNECE AND TECHNOLOGY

B.Sc. S.Y. (Sem- IV)

Examination March/April-2022 (To Be Held In June/July-2022) Electronics Paper- XII

B) 8085 Microprocessor-II

[7]:	1.52 II1	
[Time: 1:53 Hours]		[Max. Marks: 50
N.B	Please check whether you have got the right question paper. i) Attempt all questions. ii) Illustrate your answer with suitable labelled diagram.	
		7 7 7 8 8 8 7 7 7 7 7 7 7 7 7 7 7 7 7 7
Q. 1	Explain Transition state diagram in detail	20
	OR What is conditional call an return instructions techniques	20
Q. 2	What are handshake I/O data transfer techniques.	20
	OR Write short notes on any four from the following a) Types of machine cycles used in 8085 microprocessor b) Parallel data transfer techniques c) Use of stock for programmer d) 8085 interrupt structure e) Interrupt system f) Parameter passing techniques	20
Q. 3	Attempt the following multiple choice questions 1) Total no address location for 8085 microprocessor a) 244k b) 245k c) 256k d) 64k 2) are two handshake singles a) STB & IBF b) STB & INTR c) INTR d) IBF 3) 8085 microprocessor has how many pins a) 30 b) 42 c) 64 d) 40 4) Parallel transmission of data is used for a) Short distance	10
	b) Very long distance	
CAT CANA		

	c) Long distance
	d) All of these
5)	The first microprocessor to include virtual memory in the intel microprocessor family i
	a) 80386
	b) 80486
	c) 80286
	d) Pentium
6)	
	a) At the same time
	b) At the different times
	c) Can't defined
7 \	d) None of these
7)	
	a) HLT
	b) TRAP
	c) INTR
0)	d) All of these
8)	
	a) Internalb) External
	c) Both a & b
	d) Name of these
0)	What is mean by ALU
))	a) Arithmetic logic upgrade
	b) Arithmetic logic upgrade
	c) Arithmetic local unsigned
	d) Arithmetic logic unit
10	Following is a 16-bit register for 8085 microprocessor
6	a) Stack pointer
3	b) Accumulator
200	c) Register
76	d) Register C
1 1/1	. W. CY . D. P. T. C. S. L. C. CO . W. C. C. CY . C. C