

Time: One Hour

Max. Marks: 25

Instructions

- Solve any 25 questions from Q.1 to Q.30

- Microprocessor is a/an -----circuit that function as the CPU of the computer.
(A)electronic (B)mechanic (C)integrating (D)processing
- Microprocessor is the -----of the computer it performs all the computational task.
(A)main (B)heart (C)important (D)simple
- The Intel 8086 microprocessor is a -----processor.
(A)8-bit (B)16-bit (C)32-bit (D)4 bit
- The microprocessor can read /write 16-bit data from or to -----
(A)memory (B)I/O device (C)processor (D)register
- The work of EU is-----
(A)encoding (B)decoding (C)processing (D)calculation
- The CF is known as -----
(A)carry flag (B)conditional flag (C)common flag (D)single flag
- The SF is called as -----
(A)services flag (B)sign flag (C)single flag (D)conditional flag
- The register AX is formed by grouping -----
(A)AH & AL (B)BH & BL (C)CH & CL (D)DH & DL
- The SP is indicated by -----
(A)single pointer (B)stack pointer (C)source pointer (D)destination pointer
- The SS is called as-----
(A)single stack (B)stack segment (C)sequence stack (D)random stack
- The index register is used to hold -----
(A)memory register (B)offset address (C)segment memory (D)offset memory
- The BIU contains FIFO register of size ----- bytes
(A)8 (B)6 (C)4 (D)12
- The DS is called as -----
(A)data segment (B)digital segment (C)divide segment (D)decode segment
- The IP is ----- bits in length
(A)8 bits (B)4 bits (C)16bits (D)32 bits
- The PUSH source copies a word from source to -----
(A)stack (B)memory (C)register (D)destination
- LDs copies to consecutive words from memory to register and -----
(A)ES (B)DS (C)SS (D)CS
- INC destination increments the content of destination by -----
(A)1 (B)2 (C)30 (D)41
- IMUL source is signed -----
(A)multiplication (B)addition (C)subtraction (D)division
- destination inverts each bit of destination
(A)NOT (B)NOR (C)AND (D)OR
- The JS is called as -----
(A)jump the signed bit (B)jump single bit (C)jump simple bit (D)jump single bit
- The conditional branch instruction specifies ----- for branching
(A)condition (B)instruction (C)address (D)memory
- The microprocessor determines whether the specified condition exists or not by testing -----
(A)carry flag (B)conditional flag (C)common flag (D)sign flag
- The LEs copies to word from memory to register and -----
(A)DS (B)CS (C)ES (D)SS
- The ----- translates a byte from one code to another code
(A)XLAT (B)XCHNG (C)POP (D)PUSH
- The ----- is required to synchronize the internal operands in the processors CLK single
(A)UR single (B)Vcc (C)ALE (D)Ground
- The PIN of minimum mode AD0-AD15 has ----- address
(A)16-bit (B)20-bit (C)32-bit (D)4 -bit
- The PIN of minimum mode AD0 – AD15 has ----- data bus

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|---|------------|-------------------|-----------|
| (A)4-bit | (B)20-bit | (C)16-bit | (D)32-bit |
| 28 The address bits are sent out on lines through ----- | | | |
| (A)A16-A19 | (B)A0-A17 | (C)D0-D17 | (D)C0-C17 |
| 29 ----- is used to write into memory | | | |
| (A)RD | (B)WR | (C)RD/WR | (D)CLR |
| 30 If MN/MX is low the 8086 operates in ----- mode | | | |
| (A)minimum | (B)maximum | (C)both (a) & (b) | (D)medium |