

Time: One Hour

Max. Marks: 25

instruction

- solve any 25 questions from Q.1 to Q.30

- 1 8051 micro controller has _____ timers/counters of _____ bits.
 (A)2, 16 (B)4, 16 (C)2, 4 (D)4, 8
- 2 Which of the following Interrupt has highest priority?
 (A)IE0 (B)IE1 (C)TF0 (D)TF1
- 3 IE1 interrupt bit in 8051 micro controller is available in _____ register.
 (A)TCON (B)IP (C)IE (D)TMOD
- 4 Mode 0 of timer in 8051 micro controller uses _____ bits of TLX and _____ bits of THX.
 (A)5, 8 (B)8, 5 (C)8, 8 (D)8, 0
- 5 _____ helps in generation of wave forms in micro controller.
 (A)Input (B)Output (C)Timer (D)Memory
- 6 Vector address for External Interrupt 0 is _____.
 (A)0003H (B)0013H (C)000BH (D)001BH
- 7 8051 micro controller has _____ internal and _____ external interrupts.
 (A)1, 4 (B)4, 1 (C)2, 3 (D)3, 2
- 8 _____ code is used to display 5 on common cathode 7-segment display.
 (A)D6H (B)82H (C)6DH (D)28H
- 9 Pin _____ of 16x2 LCD is used to latch the data/command register.
 (A)4 (B)5 (C)6 (D)3
- 10 Stepper motor requires _____ steps to complete two rotations with step size 1.8°.
 (A)100 (B)200 (C)400 (D)800
- 11 Hex value of command code for "Increment Cursor" in 16x2 LCD is _____.
 (A)01H (B)06H (C)0CH (D)0EH
- 12 The value of TH0= _____ and TL0 = _____ for generation of a square waveform of 2KHz at a pin of 8051 micro controller with 12MHz crystal frequency in mode 1 of timer 0.
 (A)FFH, 1AH (B)1AH, FFH (C)FFH, A1H (D)FEH, 1AH
- 13 TR1 bit of timer in 8051 micro controller is available in _____ register.
 (A)TMOD (B)TCON (C)SCON (D)SMOD
- 14 PX2 interrupt bit in 8051 micro controller is available in _____ register.
 (A)TCON (B)IP (C)IE (D)TMOD
- 15 _____ is the counting rate of a machine cycle in correlation to the oscillator frequency for timers in 8051 microcontroller.
 (A)1/10 (B)1/12 (C)1 / 15 (D)1 / 20
- 16 _____ is the special function register that plays a vital role in the timer/ counter mode selection process by allocating the bits in it.
 (A)TMOD (B)TCON (C)SCON (D)PCON
- 17 _____ bit must be set in TCON register in order to start the 'Timer 0' in 'Mode 1'.
 (A)TF0 (B)IT0 (C)TR0 (D)IE0
- 18 _____ bit is used to double the baud rate of the serial communication.
 (A)SMOD (B)SCON (C)TF1 (D)TF0
- 19 _____ register of 8051 micro controller is not a bit addressable register.

Examination October 2020

- (A) TMOD (B) P0 (C) ACC (D) IE
- 20 LM34 outputs _____ per degrees Fahrenheit rise in temperature.
- (A) 5 mV (B) 10mV (C) 5V (D) 10V
- 21 _____ pin of 8051 microcontroller is multiplexed for external interrupt.
- (A) P3.0 (B) P3.1 (C) P3.2 (D) P3.4
- 22 The resolution of 8-bit ADC is _____.
- (A) 1.953% (B) 0.1953% (C) 0.977% (D) 9.77%
- 23 RI bit in 8051 micro controller is available in _____ register.
- (A) SCON (B) TMOD (C) IP (D) TCON
- 24 In mode _____, the data is transferred through RXD pin and clock pulses appear in the TXD pin of 8051 micro controller..
- (A) 0 (B) 1 (C) 2 (D) 3
- 25 In TMOD register, if M1=1 and M0=0 then Timer operates in Mode _____.
- (A) 0 (B) 1 (C) 2 (D) 3
- 26 RS232 is _____ 232 _____ port used in most of the computers.
- (A) Recommend Standard, parallel (B) Recommend Standard, serial (C) Register Standard, parallel (D) Register Standard, serial
- 27 Pin _____ of RS232 connector DB-9 is Data Carrier Detect.
- (A) 1 (B) 2 (C) 3 (D) 4
- 28 _____ bit is used to check error in serial communication.
- (A) Data (B) Start (C) Stop (D) Parity
- 29 8051 micro controller has 32 bi-directional and individual addressable lines arranged as _____ 8-bit ports P0-P3.
- (A) single (B) two (C) three (D) four
- 30 _____ pin of 8051 microcontroller is multiplexed for external data memory read strobe.
- (A) P3.0 (B) P3.2 (C) P3.4 (D) P3.7