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SUBJECT CODE NO: - YY-2392

FACULTY OF SCIENCE AND TECHNOLOGY

B.Sc. (CBCGS) (Pattern 2022) F.Y SEM II

Examination April / May - 2024

Electronics Paper-V Digital Electronics II

[Time: 1:30 Hours]

[Max. Marks: 40]

Please check whether you have got the right question paper.

N. B

- 1) All questions are compulsory.
- 2) All question carry equal marks.
- 3) Draw neat diagram and give labels wherever necessary.

Q.1 Explain J-K flip-flop with logical diagram, logical symbol and truth table? 10

OR

Explain Synchronous four bit up Counter? 10

Q.2 Explain the working of PISO shift Register? 10

OR

Explain Ladder Method of D/A conversion 10

Q.3 Write a short notes on any two. 10

- a) Explain T-type flip flop.
- b) Explain Asynchronous counter.
- c) Explain PIPO shift Register.
- d) Explain Random Access Memories (RAM)

Q.4 Multiple choice question 10

1. What is the standard form of S-R flip flop?
 - a) Set Reset
 - b) Simple - Reset
 - c) Single Reset
 - d) Simple - Root.
2. When toggle condition occurs in JK flip flop?
 - a) J=0 K=0
 - b) J=0 K=1
 - c) J=1 K=0
 - d) J=1 K=1.
3. How many inputs does D- flip flop have?
 - a) Zero
 - b) One.
 - c) Two
 - d) Four.

