Total No. of Printed Pages:2

SUBJECT CODE NO: - YY-2392

FACULTY OF SCIENCE AND TECHNOLOGY

B.Sc. (CBCGS) (Pattern 2022) F.Y SEM II

Examination April / May - 2024

Electronics Paper-V Digital Electronics II

[Time: 1:30 Hours] [Max. Marks: 40] Please check whether you have got the right question paper. 1) All questions are compulsory. N.B 2) All question carry equal marks. 3) Draw neat diagram and give labels wherever necessary. **Q.1** Explain J-K flip-flop with logical diagram, logical symbol and truth table? 10 OR Explain Synchronous four bit up Counter? 10 Q.2 Explain the working of PISO shift Register? 10 Explain Ladder Method of D/A conversion 10 Q.3 Write a short notes on any two. 10 a) Explain T-type flip flop. b) Explain Asynchronous counter. c) Explain PIPO shift Register. d) Explain Random Access Memories (RAM) Q.4 Multiple choice question 10 1. What is the standard form of S-R flip flop? a) Set Reset b) Simple - Reset c) Single Reset d) Simple - Root. 2. When toggle condition occurs in JK flip flop? a) J=0 K=0 b) J=0 K=1 c) J=1 K=0d) J=1 K=1. 3. How many inputs does D- flip flop have? a) Zero b) One. c) Two d) Four.

4.	The synchronous counter is one typ	e of	Counter.
	a) SSI	b) LSI	
	c) MSI	d) VLSI	
	Com 2022) F.V.SEM 11		5.32.6
5.	How many of states are there in 4-bit Counter?		
	a) Four Lection would be the Market M	b) Eight	W. WAHAVO
	c) Sixteen	d) Twenty	
	ranger States on purify AA's to be a con-		S LIBRARY
6.	In serial shifting method, data shifting occurg		
	a) Two bit at a time.	b) One bit at a time.	
	c) Simultaneously.	d) Four bit at a time	
7.	An 8-bit SISO mode needs	_ clock pulses to load an 8-bit number	
	into a register.		
	a) 8	b) 6	
	c) 4	d) 2	
0			
8.	Drawback of counter type A/D Counverter		
	a) More complex.		b) low speed
	c) Counter clears automatically		d) High conversion time
0	DOM stand Con	Salarik i	
9.	ROM stand for		1.) Doloot one
	a) Random only memory.		b) Robust one memory.
	c) Read only Memory.		d) Real Off Memory.
10. The resolution of an 8-bit DAC will be			
10.		N7.0 1	
	a) $\frac{1}{257}$	b) $\frac{1}{1024}$	
	c) $\frac{1}{64}$	d) $\frac{1}{255}$	An alternative State of Section 24
		233	